

### 1.25G PIN-TIA Receiver Modules

### **HEPTR-71XXX**

#### Features:

- High sensitivity
- Differential ended output
- ◆ Single +3V、+5V、3.3/5V operation
- Trans-impedance amplifier with AGC
- ◆ RoHS Compliant products available



## **Applications:**

- ◆ 1.25G Bit Rate application
- ◆ Fast Ethernet receiver/transceiver application

### General:

HEPTR-71XXXX Series is a TO-based 1.25G, 4 pin PIN-TIA. It provides high sensitivity with AGC and 100ohm differential outputs. A photodiode is mounted into a low capacitance coaxial package integrated with a receptacle.

## Ordering Information: (Standard version \*Note1)

Part No.	Wavelength (nm)	Voltage (V)	Package	Pin Type	Insulation
HEPTR-7130AJ	1270~1620	3.3	Α	Α	Y
HEPTR-7150B	1270~1620	5	В	Α	N
HEPTR-7150E	1270~1620	5	Е	Α	N
HEPTR-7130FJ	1270~1620	3.3	F	Α	Y

Note1: For more ordering information, please refer the nomenclature and contact HighEasy sales.

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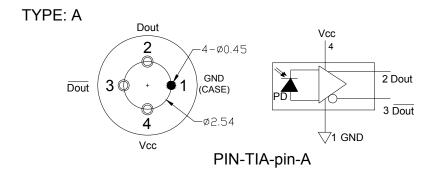
### **Absolute Maximum Ratings:**

Parameter	Min.	Тур.	Max.	Unit	
Storage Temperature	-40	25	85	$^{\circ}$	
Operating Temperature	-40	25	85	$^{\circ}$	
TIA Supply Voltage	3.1	3.3	3.5	V	
TIA Supply Voltage	4.5	5	5.5	V	
PD Reverse Voltage			15	V	
Operation Relative Humidity			85	%	
Soldering Temperature/Time			260/10	°C/s	

## **Electrical and Optical Characteristics:**

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Operating Wavelength	λ	1270		1620	nm	
Supply Current	Icc			40	mA	No Loads
Saturation Power	Psat	-3	0	-	dBm	VR=5V,25°C
Small-Signal Bandwidth	BW	700			MHz	at -3dBm
Sensitivity			-28	-25	dBm	=1310nm,1.25G, BER=10 <sup>-10</sup> @ PRBS= 2 <sup>23</sup> -1
Output Resistance	Ro		50		Ω	
Rise /Fall Time	T			4.5		10~90%

# Pin Assignment: \*Note2

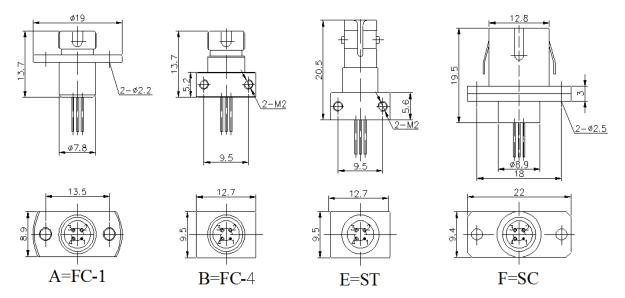


Note2: Other Pin type can be customized.

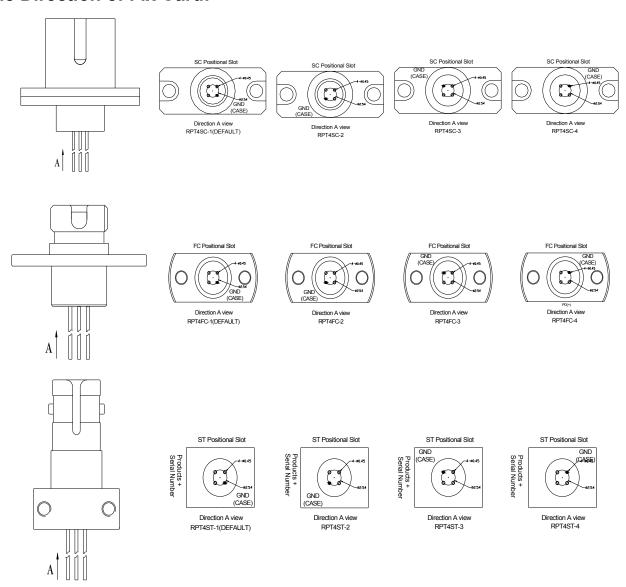
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## **Receptacle Package Series:**



### The Direction of Fix Card:



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### Nomenclature:

HEPTR— 🗆			

С

D

Ε

F

Α	Data Rate	7=1.25G					
В	Wavelength	1=1270∼1620nm					
С	Voltage	3=3.3V 5=5V 0=3.3/5V					
D	Pin Type	0= pin-A					
Е	Package Series	A=FC-1	A=FC-1 B=FC-4				FD=SC
F	Insulation	Blank=NO Insulation			J= Insulation		

#### **Precaution:**

- (1) The modules should be handled in the same manner as ordinary semiconductor devices to prevent the electro-static damages. For safe keeping and carrying, the modules should be packaged with ESD proof material. To assemble the modules on PCB, the workbench, the soldering iron and the human body should be grounded.
- (2) Please pay special attention to the atmosphere condition because the dew on the module may cause some electrical damages.
- (3) Under such a strong vibration environment as in automobile, the performance and reliability are not guaranteed.

### **Notice:**

HighEasy reserves the right to make changes or discontinue any product or service identified in this publication, without notice, in order to improve design and/or performance. Applications that are described herein for any of the products are for illustrative purposes only. HighEasy makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

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